## JC20 Rec'd PCT/PTO 2 3 SEP 2005

XA-10437 PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

NISHIZAWA, Hirotaka et al.

Intl. Appln. No.: PCT/JP2004/013930

Intl. Filing Date: 24 September 2004

For: SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Attn: Mail Stop PCT, DO/EO/US

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO-1449 are hereby cited.

The document on the attached List was cited in the specification, on pages 1-2, and its relevance is indicated therein.

Respectfully submitted,

Mitchell W. Shapiro

Reg. No. 31,568

MWS:sjk

Miles & Stockbridge P.C. 1751 Pinnacle Drive, Suite 500 McLean, Virginia 22102-3833 (703) 903-9000

September 23, 2005

JC20 Rec'd PCT/PTO 2 3 SEP 2005

			FORM PTO-1449						
INFORMATION DISCLOSURE STATEMENT LIST OF DOCUMENTS CITED BY APPLICANT					XA-10437		0/550559		
					Applicant NISHIZAWA, Hirotaka et al.				
									Filing Date
					HEREWITH				
								U.S. PATE	NT DO
Examiner Initial		Document Number	Date		Name	Class	Sub- class	Filing Date	
	AA								
	AB								
	AC			_					
	AD								
			FOREIGN PAT	PENT	DOCUMENTS				
Examiner Initial		Document Number	Date		Country	Class	Sub- class	Translation	
	AE	2000-99215	04/07/00	Japa	an			Yes	
	AF								
	AG								
	AH								
	AI								
	AJ								
	AK								
	AL								
	AM								
	AN								
	AO								
	AP								
	AQ								
	AR								
	AS								
	AT								
		OTHER (inc	cluding author, t	itle, o	date, pertinent pa	ages, etc.)	<del></del>		
	AU								
Examiner					te Considered				
609; dra	aw lir	nitial if reference through citation to	n if not in con	whether nforman	or not citation or not cons	on is in condensation is in condensation.	ontorman Include	ce with MPE copy of this	